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CONFIRMATION NO. 8220

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a 371 of PCT/JP04/15463 10/13/2004

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2003357694 10/17/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 6	<b>TOTAL CLAIMS</b> 4	<b>INDEPENDENT CLAIMS</b> 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <u>[Signature]</u> Initials <u>MT</u>				

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## TITLE

Semiconductor memory device, controller, and read/write control method thereof

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